

PHASE NOISE (1 Hz BW, typical)

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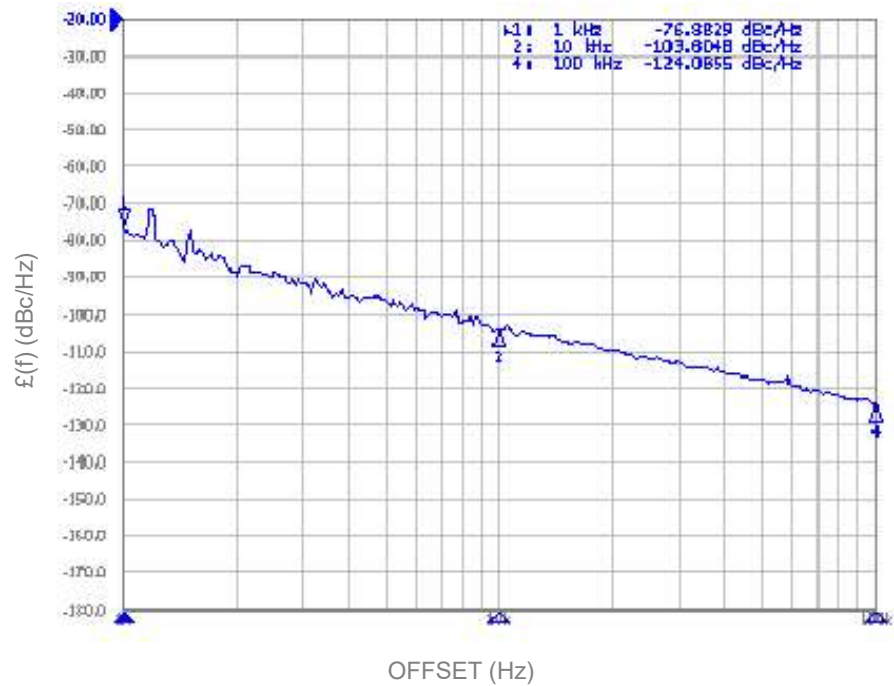
Voltage-Controlled Oscillator Surface Mount Module

Applications

Test Equipment
 Radar Equipment

Application Notes

AN-101: Mounting and Grounding
 AN-102: Output Loading
 AN-107: Manual Soldering



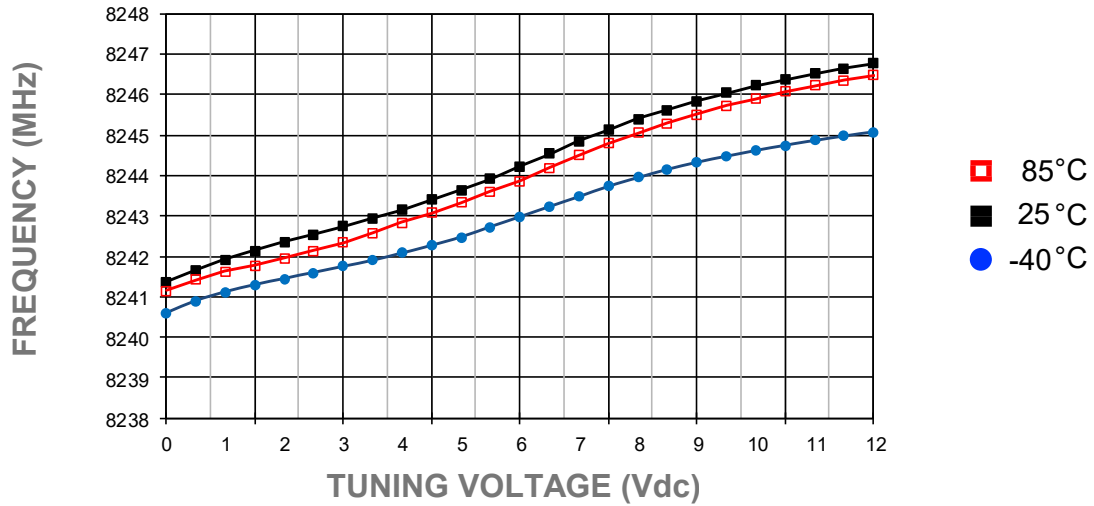
Performance Specifications	Min	Typ	Max	Units
Oscillation Frequency Range	8244		8244	MHz
Phase Noise @ 10 kHz offset (1 Hz BW)		-102		dBc/Hz
Harmonic Suppression (2nd)		-25	-20	dBc
Tuning Voltage	0		12	Vdc
Tuning Sensitivity (avg.)		0.75		MHz/V
Power Output	-3	0	3	dBm
Load Impedance		50		Ω
Input Capacitance			50	pF
Pushing			1	MHz/V
Pulling (dB Return Loss, Any Phase)			1	MHz
Operating Temperature Range	-40		85	$^{\circ}\text{C}$
Package Style		SDRO		

Power Supply Requirements	Min	Typ	Max	Units
Supply Voltage (Vcc, nom.)		5		Vdc
Supply Current (Icc)		25	30	mA

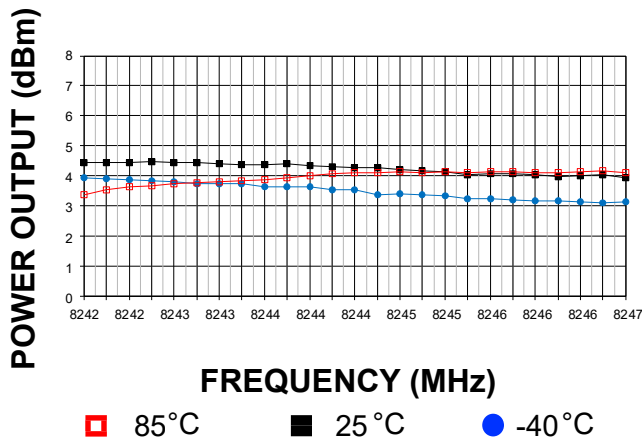
Additional Notes

It is not recommended to solder the ground plane under the DRO. Only the outside castellations shall be soldered.

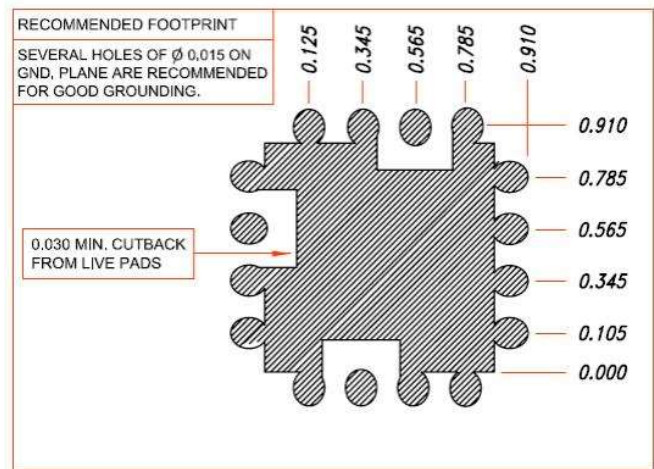
Tuning Curve, typ.



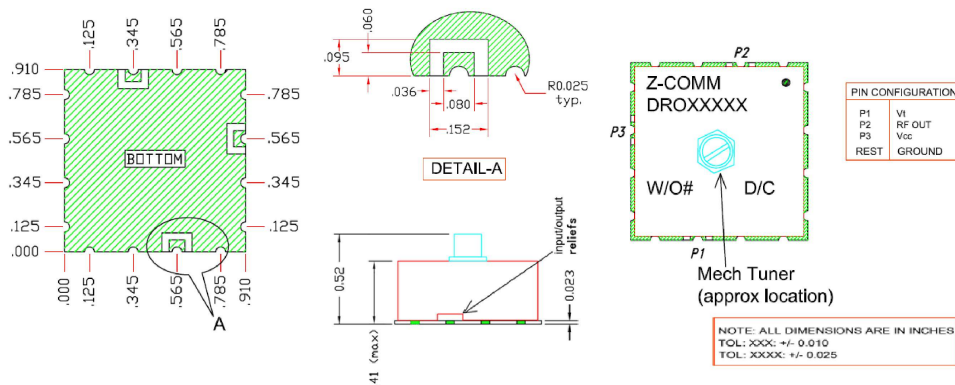
Power Curve, typ.



Footprint



Physical Dimensions



PIN CONFIGURATION	
P1	V _I
P2	RF OUT
P3	V _{CC}
REST	GROUND

NOTE: ALL DIMENSIONS ARE IN INCHES
TOL: XXX: +/- 0.010
TOL: XXXX: +/- 0.025

LFSuffix = RoHS Compliant. All specifications are subject to change without notice.