

## Programmable Phase Locked Loop Surface Mount Module

Applications	Application Notes
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|---|---|
| <ul style="list-style-type: none"> <li>Satcomm</li> <li>Telecommunications</li> <li></li> </ul> | <ul style="list-style-type: none"> <li>AN-107: Manual Soldering Technique</li> <li>AN-202: Programming Z-COMM PLLs</li> </ul> |
|---|---|

Performance Specifications	Min	Typ	Max	Units
Frequency	2025		2226	MHz
Phase Noise @ 10 kHz offset (1 Hz BW)		-94		dBc/Hz
Harmonic Suppression (2nd)		-15		dBc
Spurious Suppression		-65		dBc
Power Output	-5	0	5	dBm
Load Impedance		50		$\Omega$
Step Size		100		kHz
Phase Detector Frequency		100		kHz
Settling Time		3		mS
Charge Pump Output Current		5000		$\mu$ A
Operating Temperature Range	-40		85	$^{\circ}$ C
Package Style	PLL-V12C			

Power Supply Requirements	Min	Typ	Max	Units
Supply Voltage 1: PLL (Vcc, nom)		3		Vdc
Supply Voltage 2: VCO (Vcc, nom)		5		Vdc
Supply Current 1: PLL (Icc, typ)		10		mA
Supply Current 2: VCO (Icc, typ)		21		mA

Reference Oscillator Signal	Min	Typ	Max	Units
Frequency		10		MHz
Phase Noise @1 kHz Offset		-145		dBc/Hz

Additional Notes
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Phase Locked Loop Internal IC: Analog Devices / ADF4106

LFSuffix = RoHS Compliant. All specifications are subject to change without notice.

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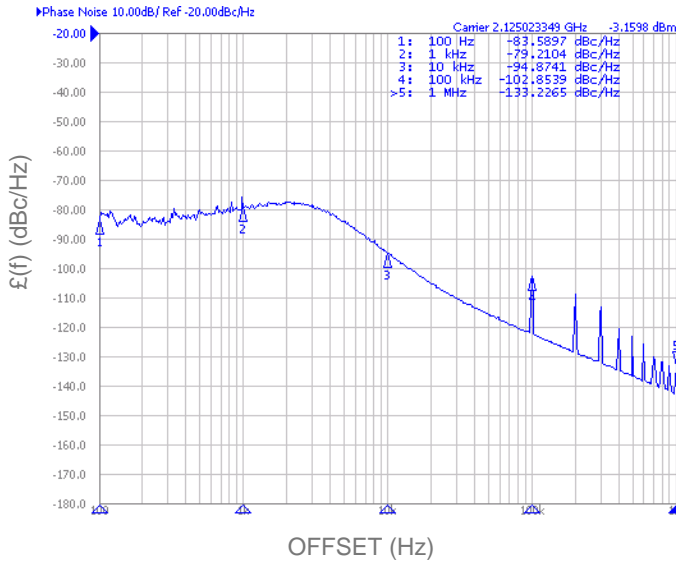
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Programmable Phase Locked Loop  
Surface Mount Module

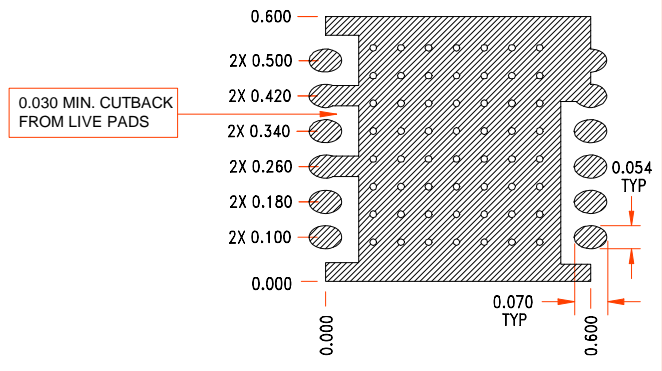
Phase Noise, typ.

Footprint

PHASE NOISE (1 Hz BW, typical)

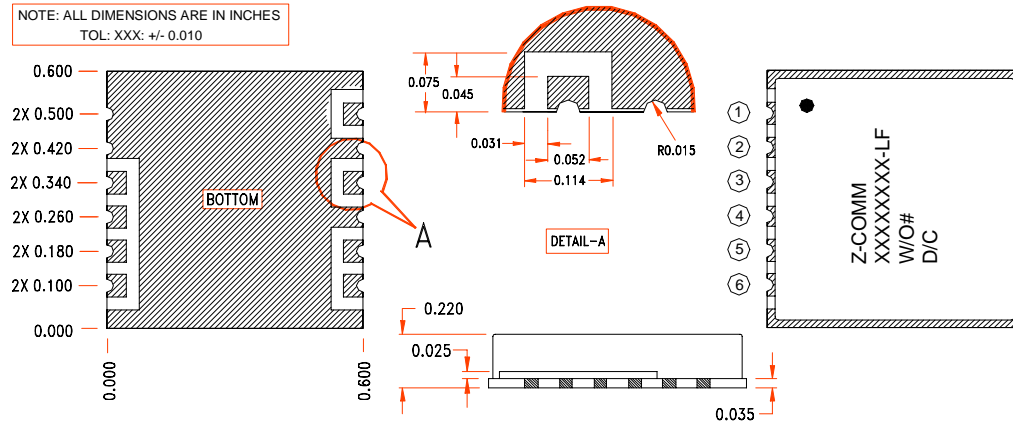


**RECOMMENDED FOOTPRINT**  
SEVERAL HOLES OF  $\phi$  0.015 ON GND. PLANE ARE RECOMMENDED FOR GOOD GROUNDING.



Physical Dimensions

NOTE: ALL DIMENSIONS ARE IN INCHES  
TOL: XXX: +/- 0.010



SFS PIN CONFIGURATION	
1	Vcc (VCO)
3	RF OUT
5	MUX OUT
6	Vcc (CHIP)
8	N/C
10	REF IN
REST	GROUND

PVA PIN CONFIGURATION	
1	Vcc (VCO)
3	RF OUT
5	MUX OUT
6	Vcc (CHIP)
7	CLOCK
8	DATA
9	ENABLE
10	REF IN
REST	GROUND